

MIC_DATA PIN CONNECTIONS ON THE XVF3510-QF60-C

To ensure compatibility with future updates of the XVF3510 firmware, please note the requirement for connection of two MIC_DATA pins (pins 31 and 43) to the data output from the PDM microphones. See below for excerpts from XVF3510 Datasheet highlighting the connections.

Name	Description	Pin	I/O
MIC_DATA	Mic array data. Two standard PDM MEMS microphones should be connected to the MIC_DATA pins, setting one microphone to be left (output on rising edge of clock) and the other right (rising on the falling edge of clock). Pin 43 and Pin 31 are connected to the same trace on the PCB. This trace is routed to two pins.	31 43	I

Table 1 - Boot pins

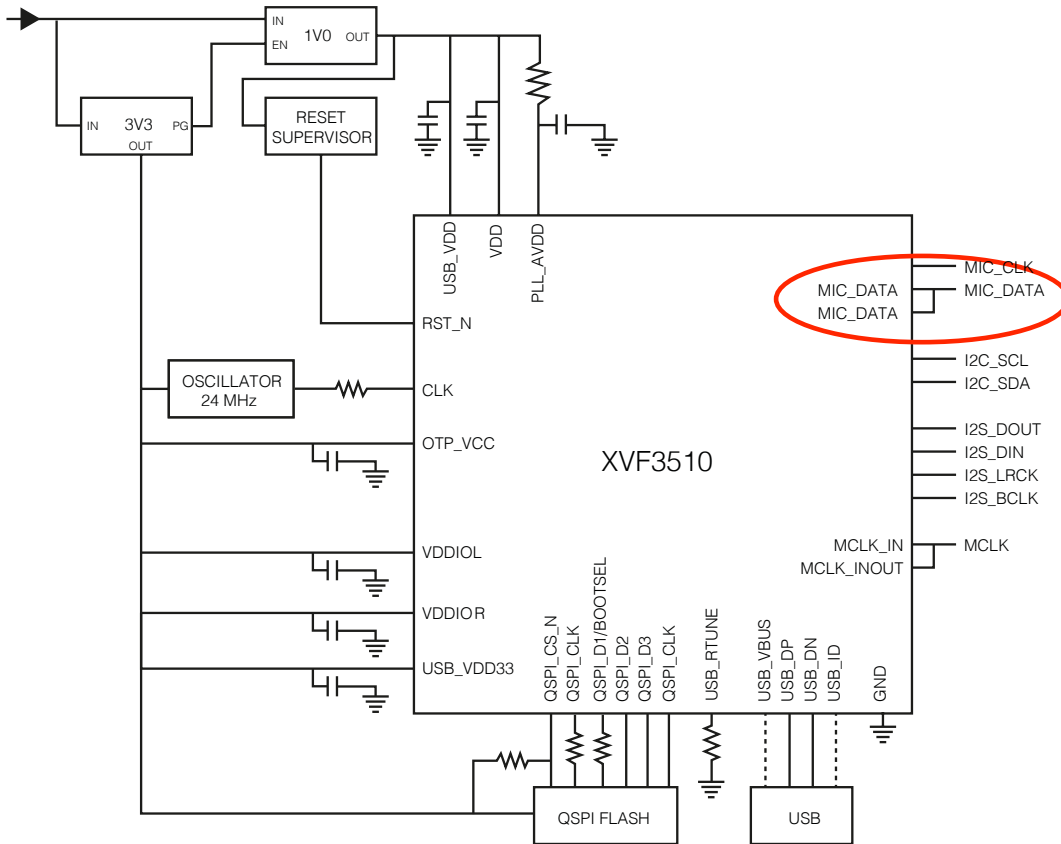


Figure 2 - XVF3510 example application diagram showing MIC_DATA connections

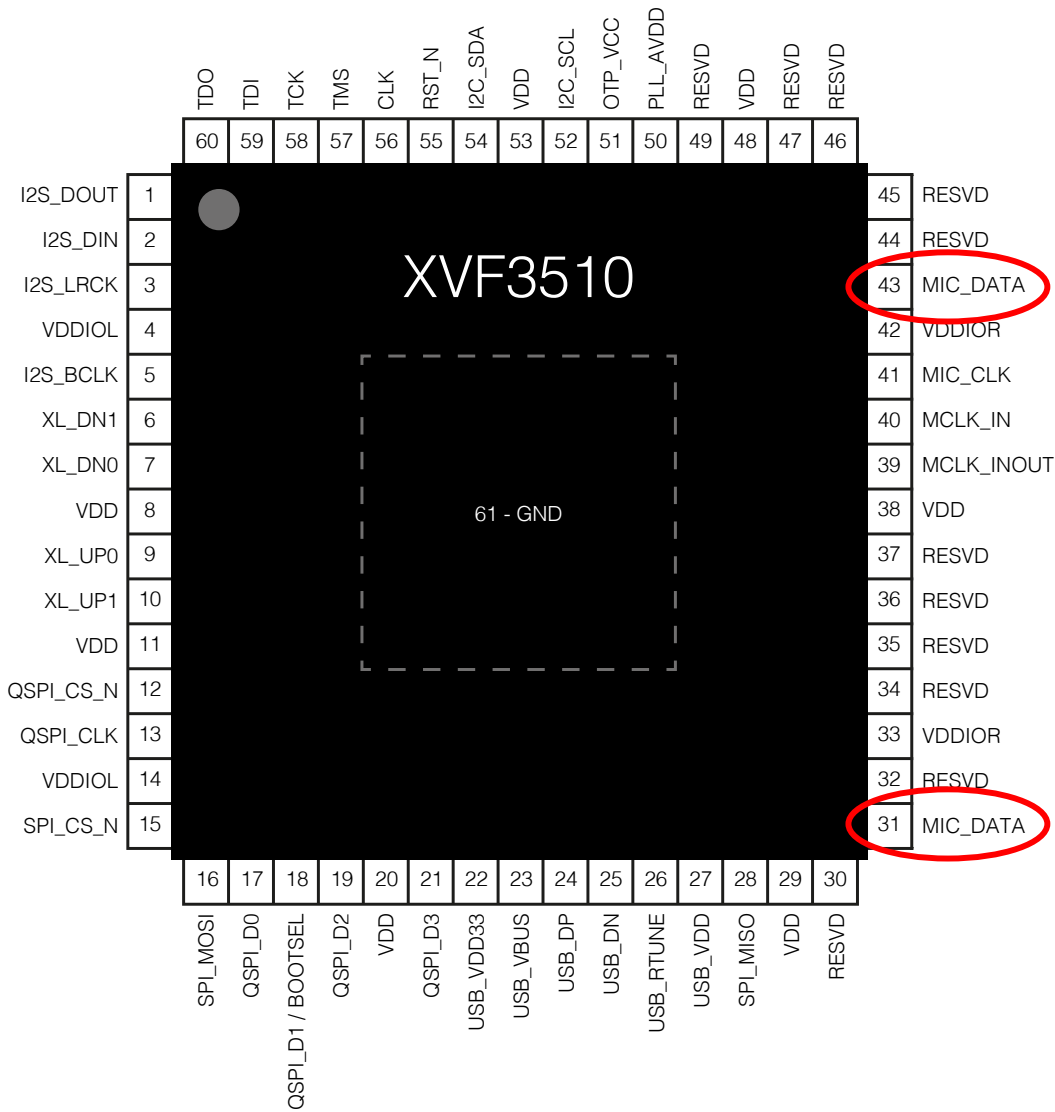


Figure 3 – XVF3510 pin diagram showing MIC_DATA connections