

## Mandelbrot (MBROT)

The Mandelbrot demonstration uses the parallel feature of the XMOS architecture to farm out the processing to multiple threads running across the XCores.

The generation manager requests Mandelbrot calculations from eight threads running the escape time algorithm. It then passes the resulting pixel data to the SRAM server for storage.

In parallel, the SRAM server pipes the RGB data to the LCD driver, which displays the image on the LCD.

