

Display Controller Component

REV A

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1 Overview

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The display controller component is used to drive a single graphics LCD screen up to 800 * 600 pixels incorporating a managed double buffer.

1.1 Features

- ▶ Non-blocking SDRAM management.
- ▶ Real time servicing of the LCD.
- ▶ Image memory manager to simplify handling of images.
- ▶ No real time constraints on the application.

1.2 Memory requirements

Resource	Usage
Stack	6198 bytes
Program	11306 bytes

1.3 Resource requirements

Resource	Usage
Channels	3
Timers	0
Clocks	0
Threads	1

1.4 Performance

The achievable effective bandwidth varies according to the available XCore MIPS. The maximum pixel clock supported is 25MHz.

2 Hardware Requirements

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2.1 Recommended Hardware

2.1.1 Slicekit

This module may be evaluated using the Slicekit Modular Development Platform, available from digikey. Required board SKUs are:

- ▶ XP-SKC-L2 (Slicekit L2 Core Board)
- ▶ XA-SK-SCR480 plus XA-SK-XTAG2 (Slicekit XTAG adaptor)

2.2 Demonstration Applications

2.2.1 Display Controller Application

- ▶ Package: `sw_display_controller`
- ▶ Application: `app_display_controller`

This combination demo employs the `module_lcd` along with the `module_sdram` and the `module_display_controller` framebuffer framework component to implement a 480x272 display controller.

Required board SKUs for this demo are:

- ▶ XP-SKC-L2 (Slicekit L2 Core Board) plus XA-SK-XTAG2 (Slicekit XTAG adaptor)
- ▶ XA-SK-SDRAM
- ▶ XA-SK-SCR480 (which includes a 480x272 colour touch screen)

3 API

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- ▶ component: `sc_sdram_burst` which handles the SDRAM
- ▶ component: `sc_lcd` which handles the LCD

The below section details the APIs in the application. For details about the LCD and SDRAM APIs please refer to the respective repositories.

3.1 Configuration Defines

The `module_display_controller` can be configured via the header `display_controller_conf.h`. The module requires nothing to be additionally defined however any of the defines can be overridden by adding the header `display_controller_conf.h` to the application project and adding the define that needs overriding. The possible defines are:

DISPLAY_CONTROLLER_MAX_IMAGES

This defines the storage space allocated to the display controller for it to store image metadata. When an image is registered with the display controller its dimensions and location in SDRAM address space are stored in a table. The define specifies how many entries are allowed in that table. Note, there is no overflow checking by default.

DISPLAY_CONTROLLER_VERBOSE

This define switches on the error checking for memory overflows and causes verbose error warnings to be emitted in the event of an error.

3.2 API

- ▶ `display_controller_client.xc`
- ▶ `display_controller_internal.h`
- ▶ `display_controller.xc`
- ▶ `display_controller.h`
- ▶ `transitions.h`
- ▶ `transitions.xc`

The display controller handles the double buffering of the image data to the LCD as real time service and manages the I/O to the SDRAM as a non-real time service.

The display controller API is as follows: .. doxygenfunction:: display_controller .. doxygenfunction:: image_read_line .. doxygenfunction:: image_read_line_p .. doxygenfunction:: image_write_line .. doxygenfunction:: image_write_line_p .. doxygenfunction:: image_read_partial_line .. doxygenfunction:: image_read_partial_line_p .. doxygenfunction:: register_image .. doxygenfunction:: wait_until_idle .. doxygenfunction:: wait_until_idle_p .. doxygenfunction:: frame_buffer_commit .. doxygenfunction:: frame_buffer_init

The transition API is as follows: .. doxygenfunction:: transition_wipe .. doxygenfunction:: transition_slide .. doxygenfunction:: transition_roll .. doxygenfunction:: transition_dither .. doxygenfunction:: transition_alpha_blend

The transitions use the display controller API.

4 Programming Guide

IN THIS CHAPTER

- ▶ Source code structure
 - ▶ Executing The Project
 - ▶ Software Requirements
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4.1 Source code structure

Project	File	Description
module_display_controller	display_controller.h	Header file containing the APIs for the display controller component.
	display_controller.xc	File containing the implementation of the display controller component.
	display_controller_client.xc	File containing the implementation of the display controller client functions.
	display_controller_internal.h	Header file containing the user configurable defines for the display controller component.
	transitions.h	Header file containing the APIs for the display controller transitions.
	transitions.xc	File containing the implementation of the display controller transitions.

Figure 1:
Project
structure

4.2 Executing The Project

The module by itself cannot be build or executed separately. It must be linked in to an application. Once the module is linked to the application, the application can be built and tested for driving a LCD screen.

1. The module name `module_display_controller` should be added to the list of MODULES in the application project build options.
2. The module name `module_lcd` should be added to the list of MODULES in the application project build options.
3. The module name `module_sdram` should be added to the list of MODULES in the application project build options.
4. Now the module is linked to the application and can be directly used

4.3 Software Requirements

The module is built on XDE Tool version 12.0 The module can be used in version 12.0 or any higher version of xTIMEcomposer.

5 Example Applications

IN THIS CHAPTER

- ▶ `app_display_controller_demo`
 - ▶ Application Notes
-

This tutorial describes the demo applications included in the XMOS Display Controller software component. §2.1 describes the required hardware setups to run the demos.

5.1 `app_display_controller_demo`

This application demonstrates how the module is used write image data to the LCD screen whilst the the application has no real time constraints imposed on it by the `lcd_module`. The purpose of this application is to show how data is passed to the `display_controller`.

5.2 Application Notes

5.2.1 Getting Started

1. Plug the XA-SK-LCD Slice Card into the 'STAR' slot of the Slicekit Core Board
2. Plug the XA-SK-SDRAM Slice Card into the 'TRIANGLE' slot of the Slicekit Core Board
3. Open `app_display_controller_demo.xc` and build the project.
4. run the program ensuring that it is run from the project directory where the tga images are.

The output produced should look like a series of images transitioning on the LCD screen.



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